

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph at column 5, lines 3-21, of U.S. Patent No. 6,300,810 with the following amended paragraph:

The voltage at which the output of comparator ~~[[100]]~~ 101 switches is referred to as the "trip-point". The trip-point is centered at VREF as shown in FIG. 4. The hysteresis voltage (indicated by .DELTA.V in FIG. 4) is selected by the size of transistors 304 and 314 which is determined when comparator 101 is designed to meet the needs of a particular application. Wider transistors result in a larger hysteresis voltage. In the particular example both transistor 304 and 314 are similarly sized to provide symmetric hysteresis. However, the transistors can have different sizes to provide asymmetric hysteresis if desired. Alternatively, transistor 304 can be implemented by a plurality of parallel coupled transistors that can be individually programmably coupled to the VREF signal by, for example, mask programmable or field programmable techniques. This latter technique allows the hysteresis voltage to be programmed. The benefits of the present invention are greatly exploited in hysteretic DC-DC converters where the accuracy and speed of the comparator are important.